



Serial No.: 09/408,429
Filed: September 29, 1999
Attorney Case No.: EMC2-032PUS (Formerly 07072/086001)

REMARKS

The specification has been amended to make the text agree with FIG. 9 and the description of the operation of the circuit on page 29.

The Examiner is respectfully invited to telephone the undersigning attorney regarding this amendment or this application.

The Assistant Commissioner is hereby authorized to charge payment of any additional fees associated with this communication or credit any overpayment to Deposit Account No. 50-0845.

Respectfully submitted,

Date

3/9/02

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Attachment: Sheet with markings showing changes made



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Version of Specification With Markings To Show Changes

Referring now to FIG. 9, a circuit 400 is shown for detecting a hard fault (i.e., a ground fault or a connection to a fixed, unchanging voltage) on a bi-directional data line 402. The circuit 400 includes an input/output terminal 304 connected to one end of the bi-directional line 402; a data receiver 406 having an input coupled to a second end of the bi-directional line 402 for receiving data on the terminal 404; an data driver 408 having an output coupled to second end of the bi-directional line 402 for producing data on the terminal 404; an XOR gate 410 having a pair of inputs, one being coupled to an input~~output~~ of the data driver 408 and the other being coupled to an ~~input~~output of the data receiver 408, for determining whether data produced by the data driver is received by the data receiver.